## **TRANSMITTAL**

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR REDUCED ELECTRICAL FUSING TIME
Application Number :	
Date :	
First Named Applica	nt: Darren L. Anand
Confirmation Numbe	vr:
Attorney Docket Nun	nber: BUR920020093US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files		
us-request	BUR920020093US1-usrequ.xml		
•	us-request.dtd		
	us-request.xsl		
us-assignment	BUR920020093US1-usassn.xml		
· ·	us-assignment.xsl		
	us-assignment.dtd		
	BUR920020093assign1.tif		
	BUR920020093assign2.tif		
us-fee-sheet	BUR920020093US1-usfees.xml		
	us-fee-sheet.xsl		
	us-fee-sheet.dtd		
us-ids	BUR920020093US1-usidst.xml		
	us-ids.dtd		
	us-ids.xsl		
us-declaration	BUR920020093dec1.tif		
us-declaration	BUR920020093dec2.tif		
us-declaration	BUR920020093dec3.tif		
application-body	BUR920020093spec-trans.xml		
•	us-application-body.xsl		
	application-body.dtd		
	wipo.ent		
	mathml2.dtd		
	mathml2-qname-1.mod		
	isoamsa.ent		
	isoamsb.ent		
	isoamsc.ent		
	isoamsn.ent		
	isoamso.ent		
	isoamsr.ent		
	isogrk3.ent		
	isomfrk.ent		
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	isocyr1.ent		
	isocyr2.ent		
	isodia.ent		
	isolat1.ent		
	isolat2.ent		
	isonum.ent		
	isopub.ent		
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	mmlalias.ent		
	soextblx.dtd		
	BUR920020093FIGSsheet1of4.tif		
	BUR920020093FIGSsheet2of4.tif		
	BUR920020093FIGSsheet3of4.tif		
	BUR920020093FIGSsheet4of4.tif		

Comments			
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